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| R.V.R. & J.C.COLLEGE OF ENGINEERING, GUNTUR-522019  (Autonomous)  DEPARTMENT OF COMPUTER SCIENCE AND BUSINESS SYSTEMS | | | | | | | | |
| CB212 COMPUTER ORGANIZATION &ARCHITECTURE | | | | | | | | |
| AY: 2023-2024 | | | Semester III [Second Year] | | | Max. Time: 135 Min. | | |
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|  | | | Assignment Test I | | | Max. Marks:12 | | |
|  | | | | | | Marks | CO | Blooms  Level |
| 1 | List and explain briefly about the processor registers. | | | | | 6 | 1 | 2 |
| 2. | Explain about the Instruction execution cycle with an example. | | | | | 6 | 1 | 2 |
| 3 | Explain about addressing modes with an examples. | | | | | 6 | 1 | 2 |
| 4 | Describe the programmed I/O transfer. | | | | | 6 | 2 | 3 |
| 5 | Discuss about the hardwired Control unit. | | | | | 6 | 2 | 2 |
| 6 | Briefly explain about the USB. | | | | | 6 | 2 | 2 |
|  | | | | Sessional Test I | Max. Marks: 18 | | | |
| Answer ALL questions | | | | | | | | |
| 1 | a. | State the differences between Combinational circuit and Sequential Circuit. | | | | 1 | 1 | 1 |
|  | b. | Briefly explain about Register Transfer and Register Transfer language. | | | | 1 | 1 | 2 |
|  | c. | Explain about the floating point representation with an example | | | | 1 | 1 | 2 |
|  | d. | Define Non-Priviled instructions with an example. | | | | 1 | 2 | 1 |
|  | e. | What is the role of DMA Controller? | | | | 1 | 2 | 1 |
|  | f. | Explain about micro-programmed control unit. | | | | 1 | 2 | 2 |
|  | | | | | | | | |
| 2 | Explain about the Functional blocks of a computer. | | | | | 6 | 1 | 2 |
| OR | | | | | | | | |
| 3 | Explain about the various computer instruction sets. | | | | | 6 | 1 | 2 |
|  | | | | | | | | |
| 4 | Demonstrate the I/O device interface. | | | | | 6 | 2 | 3 |
| OR | | | | | | | | |
| 5 | Differentiate between Hardwired Control unit and Micro programmed control unit. | | | | | 6 | 2 | 2 |

**Note: For Q. No. from 1 to 6 in Assignment Test & Q. No. 2 to 5 in Sessional Test Two sub-questions (like a, b) with proper split of marks or single questions for 6 marks may be given.**